	Application No.	Applicant(s)
Notice of Allowability	10/814,438	MATSUMOTO, SHOICHIRO
	Examiner	Art Unit
	David L. Lewis	2629
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to 12/3/2007 and 2/4/2008.		
2. The allowed claim(s) is/are <u>10-12</u> .		
3.		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Dateii/20/2007 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Informal P 6. ☐ Interview Summary Paper No./Mail Dat 7. ☑ Examiner's Amendn 8. ☑ Examiner's Stateme 9. ☐ Other	(PTO-413), e

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EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Daniel P. Lent on 2/4/2008 by phone message.

The application has been amended as follows:

The subject matter of claim 13 has been placed into claim 11, and claim 13 has been canceled.

11. (Amended) An electroluminescence display having an electroluminescence element in each of a plurality of pixels arranged in a matrix form for achieving a display by controlling light emission from each pixel, wherein

each of a plurality of data lines is provided corresponding to each column of the matrix and a different data line among the plurality of data lines is connected to corresponding pixels for each row of the matrix; and

display data is sequentially supplied from the plurality of data lines for pixels of each column of the matrix;

wherein two control lines are provided for each row of the matrix;

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each of the pixels has a plurality of transistors controlled by the two control lines; and the writing of data voltage signal and the writing of the data current signal into each of the pixels are controlled by the two control lines.

Claim 13 (canceled).

End.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to David L. Lewis whose telephone number is 571 272-7673. The examiner can normally be reached on M, T, TH, F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on 703 305-4938. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

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REASONS FOR ALLOWANCE

- 1. Claims 10-12 are allowed over the prior art of record.
- 2. Claims 1-9 and 13 have been canceled.
- 3. The following is an examiner's statement of reasons for allowance: The Applicant's arguments filed on 12/3/2007 are persuasive. Applicant approved the Examiners amendment combing the subject matter of claims 11 and 13, and canceling claim 13. The feature of independent claim 10 directed towards allowable subject matter is a first write control transistor having a first region connected to a connection portion between the driver transistor and the drive cun-ent control transistor and a second region connected to a data line, and a second write control transistor having a first region connected to the data line and a second region connected to the gate of the driver transistor. The feature of independent claim 11 directed towards allowable subject matter is wherein two control lines are provided for each row of the matrix, and each of the pixels has a plurality of transistors controlled by the two control lines, and the writing of data voltage signal and the writing of the data current signal into each of the pixels are controlled by the two control lines. As applied to claim 10 Okabe et al. teaches of a like display device, but fails to teach of the above claimed features that requires said specific transistor connections. As applied to claim 11, Jang et al. fails to teach of said control line causing a data voltage signal and data current signal to be written. Therefore the above said features in combination with the other limitations of the above independent claims 10 and 11, and therefore claims 10-12, are found to be allowable over the prior art of record.

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- 4. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **David L. Lewis** whose telephone number is (571) 272-7673. The examiner can normally be reached on MTWTHF from 8 to 5. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala, can be reached on (571) 272-7681. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571)-273-8300.
- 6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Examiner: David L. Lewis

February 4, 2008